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OIPE JC183 AUG 16 2001 PATENT & TRADEMARK OFFICE

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APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/848,753	05/03/2001	Jeffrey N. Weiss	9889.6815

CONFIRMATION NO. 7827

FORMALITIES LETTER

OC000000006245779

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1936 South Andrews Avenue
Fort Lauderdale, FL 33316

Date Mailed: 06/29/2001

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
  - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

A copy of this notice MUST be returned with the reply.

MDwong

Customer Service Center
Initial Patent Examination Division (703) 308-1202
PART 2 - COPY TO BE RETURNED WITH RESPONSE

OIPE JC1
AUG 16 2001
PATENT & TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

August 10, 2001

In re application of	:	JEFFREY N. WEISS
Serial No.	:	09/848,753
Filed	:	May 3, 2001
Invention	:	**SELECTIVE TELEPHONE BLOCKER WITH TIMER**
Our File No.	:	9889.6815

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Hon. Commissioner of Patents and Trademarks
Attn: Customer Service Center
Initial Patent Examination Division
Washington, D.C. 20231

Dear Sir:

In response to the Notice to File Corrected Application Papers dated June 29, 2001, copy enclosed, the Applicant submits herewith substitute drawings (Figs. 1-4) in compliance with 37 CFR 1.84.

Any additional charges, including extensions of time, please bill our Account No. 13-1130.

Respectfully submitted,

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TOSTAO' ES28A860

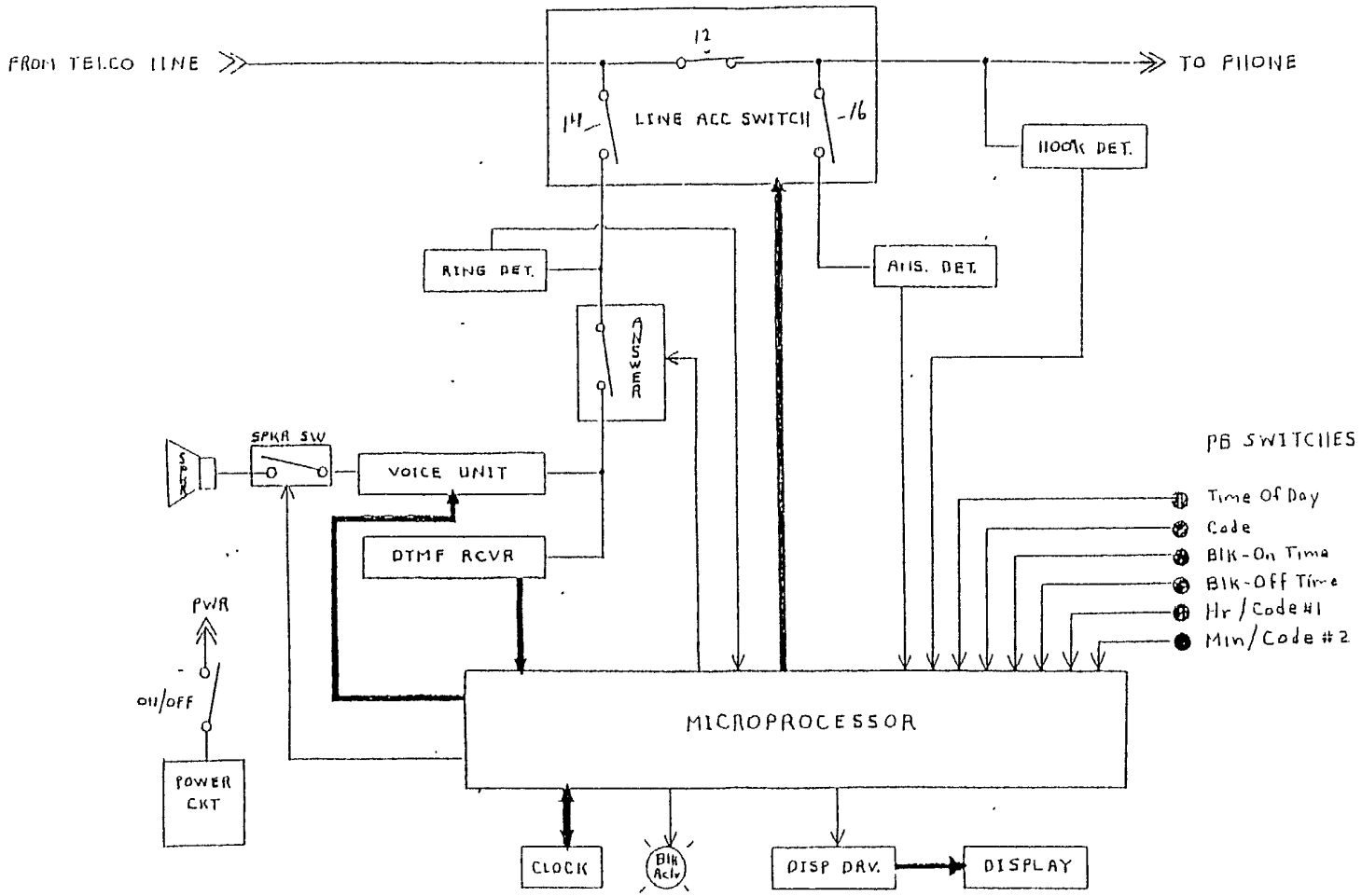

FIG. 1

TELEPHONE BLOCKER - CIRCUIT SCHEMATIC

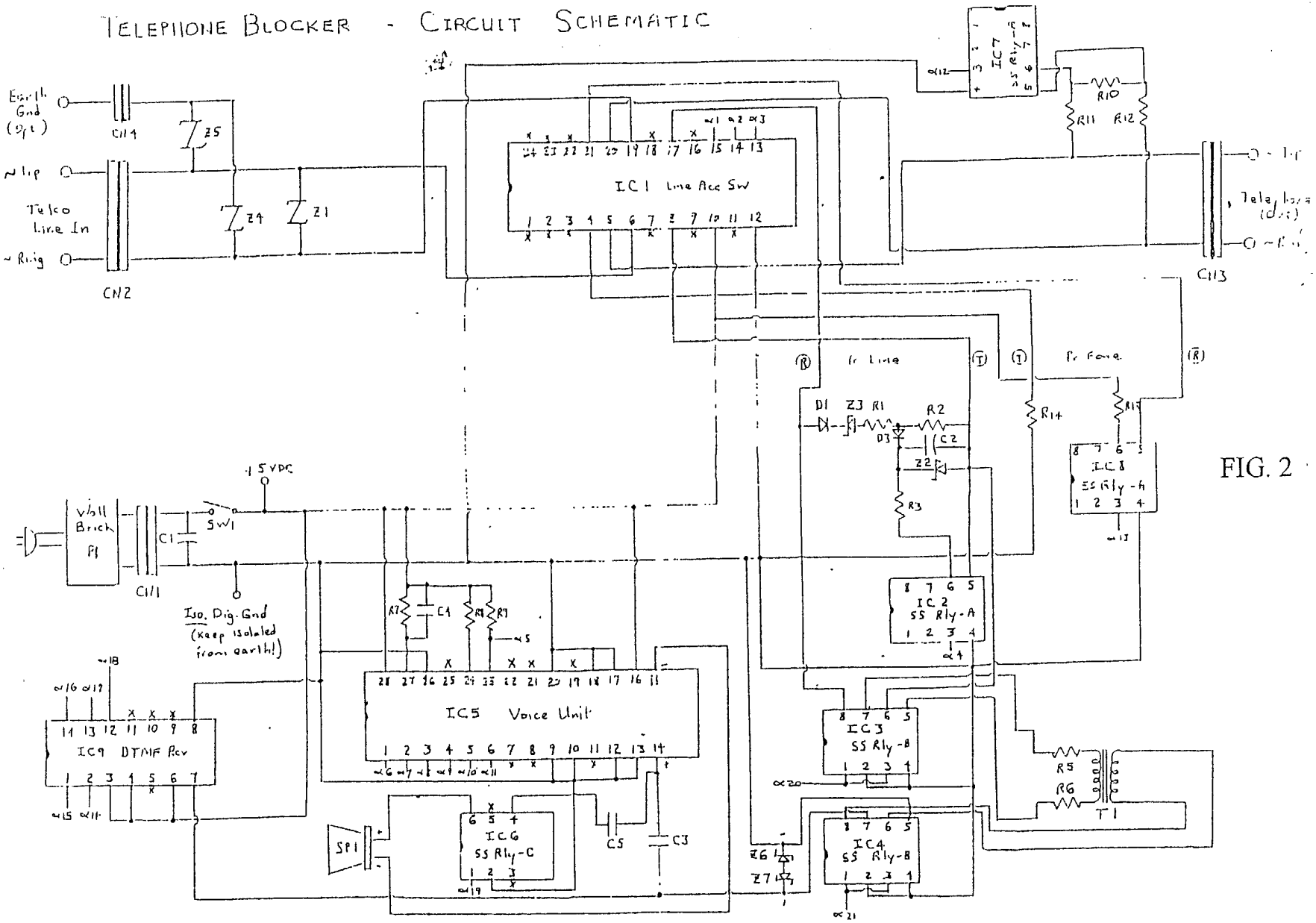

FIG. 2

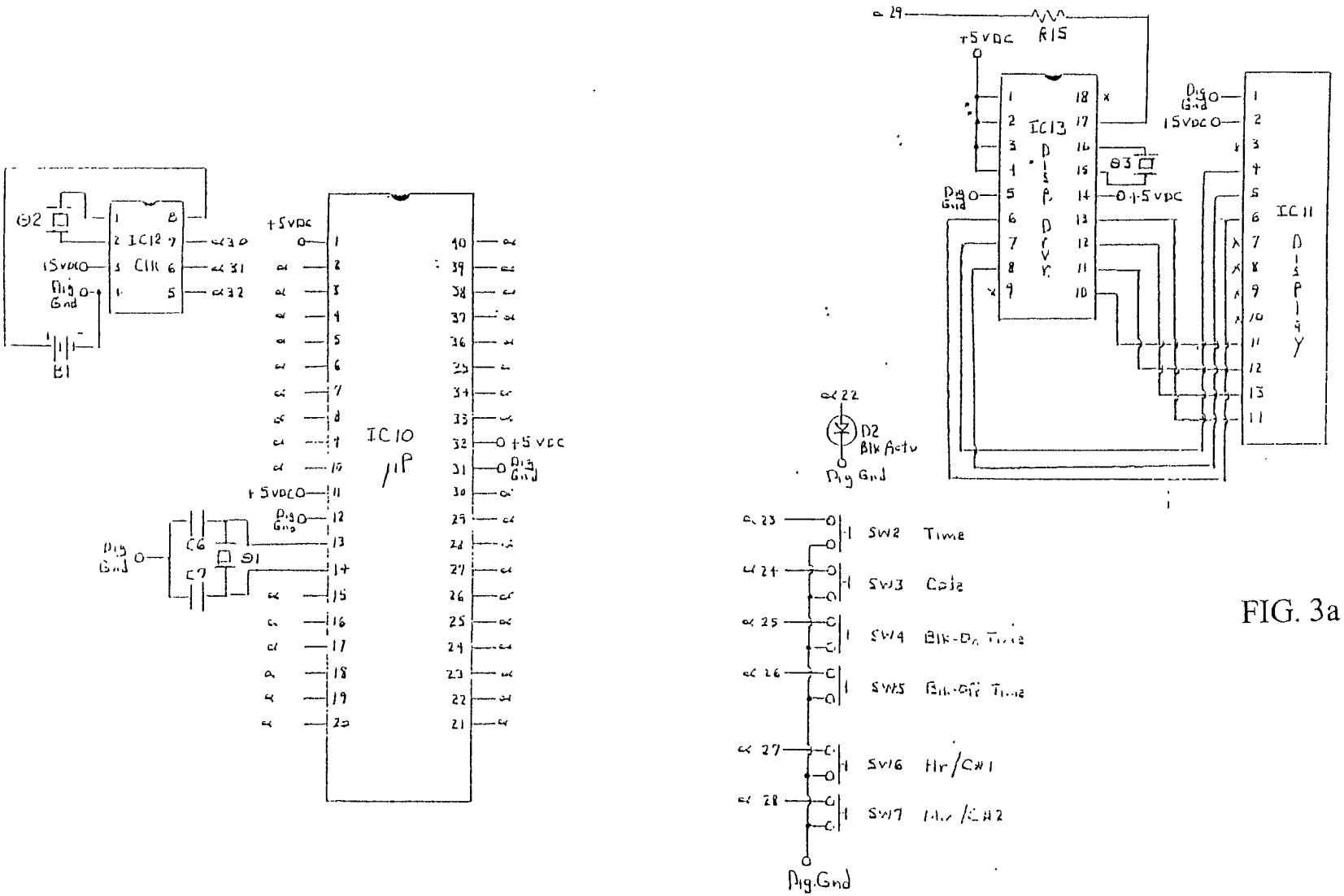
IC10
IC11
IC12
IC13
+5VDC
Dig Gnd
D2
Blk Actv
SW2 Time
SW3 Date
SW4
SW5
SW6 Hr/CH1
SW7 Min/CH2
B1
B2
B3
C6
C7
Y1
R15

FIG. 3a

DESIGNATION	DESCRIPTION	PART#	SOURCE
P1	Power Brick 5v 1a	T310	A
C1	470uf	4107PHCT	A
IC1	Line Cd Acc Sw (Lucent)	ATTL7583BF	B
Z1 Z4 Z5	Surge Abs 430v 2500a	P7257	A
CN1	Power Jack	CP-3504	A
CN2 CN3	Telco Mod Connector	A9077	A
D1 D3	1N4004	1N4004DICT	A
R1	5.1K 2w	P5.1KW-2BK	A
C2	100uf	4126PHCT	A
R2	510		
Z2 Z6 Z7	4.7v 0.5w	1N5230BMSCT	A
R3	1.5K		
IC2 IC7 IC8	SS Iso Rly (Clare)	CLA108	A
IC3 IC4	SS Iso Rly (Clare)	252-004	C
Z3	51v 1w	1N4757AMSCT	A
R5 R6	10		
C3 C5	0.47uf	P4967	A
R7 R8 R9	100K		
C4	0.001uf	P4898	A
IC5	Voice Device (ISD)	ISD1420P	A
CN4	(Opt) Gnd Post		
T1	Xfmr 600:600	10624	A
IC6	SS Rly Sw (Clare)	252-001	C
SP1	Speaker	P9613	A
R10 R13 R14	300		
R11 R12	5.1K		
IC9	DTMF Rcvr (Motorola)	MC145436AP	C
SW1	SPST	CKN1004	A
IC10	uP (Microchip)	PIC16F877	D
IC11	Display (Seiko M16410A)	6529-OP	E
D2	LED	MR3350QT	A
SW2,3,4,5,6,7	SPST-NO	P8076SCT	A
IC12	Clock	DS1302	F
IC13	Display Driver	EDE702	G
O1 O3	Xtal 4MHz	CTX-006	A
O2	Xtal 32.768KHz	X801	A
B1	Battery 3v Li	P183	A
C6 C7	15pf	P4014A	A
R15	33K		

OTHER MISC PARTS

Printed Circuit Board
Panel/Box
Bypass Capacitors
Test Points
Display Connector
IC Sockets (8,14,14,18,24,28,40 pin DIP)

FIG. 3b

6,385,305 B1

SOURCE REFERENCES

DigiKey	A
Lucent	B
Allied	C
Microchip	D
MP Jones Assoc	E
Dallas Semiconductor	F
E-Lab	G

ALPHA REFERENCES (Microprocessor Digital I/O)

1 2 3	Line Access Sw Control	DO
4	Ring Detect	DI
5	Play Voice	DO
6 7 8 9 10 11	Voice Address	DO
12	Hook Detect	DI
13	Answer Detect	DI
14 15 16 17	DTMF Data	DI
18	DTMF Valid Data	DI
19	Speaker Switch	DO
20	Answer1	DO
21	Answer2	DO
22	Blocking Active	DO
23 24 25 26 27 28	Push Button Inputs	DI
29	Display	DO
30 31 32	Clock I/F	DI/DO

FIG. 3c

Selective Telephone Blocker With Timer

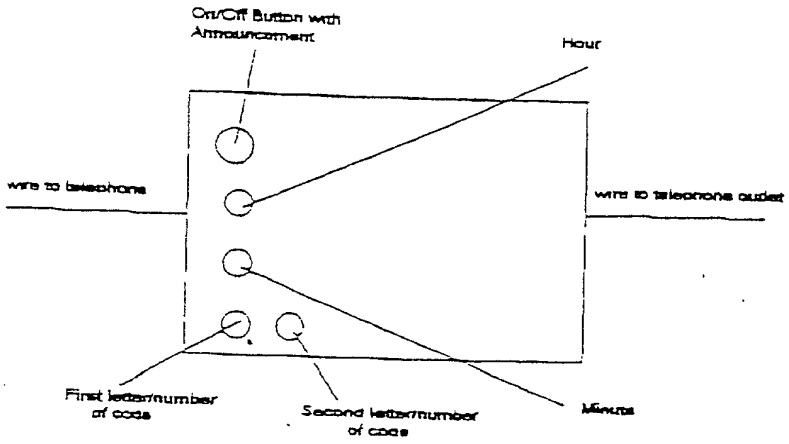

FIG. 4